Description :

Microprocessor Trainer

Technical Specification :

Microprocessor Trainer can interface with IBM PC/XT/AT computer with the help of 96 bit TTL I/O experimental interface and editor assembler software. It is quite an efficient system based on industry standard intel `ip for the purpose of training and prototype development of microprocessor based systems. Its hardware is capable of supporting multi processor configuration also.

The unit is self contained and requires no other apparatus. Practical experience on this board carries great educative value for science and engineering students.

The microprocessor trainer is designed to teach the subject of microprocessor programming and interfacing through practical, essentially designed to teach the study of :

- Design student projects
- Familiarize components used in Microprocessor based instruments
- Study the signals generated by the Microprocessor
- Learn the art of programming in machine language
- Design application oriented equipment
- Design and debugging techniques, etc.
- Perform input / output experiments

Hardware Specifications:

- CPU : Microprocessor @6.144 MHz
- MEMORY : Total space ON BOARD is 48KB,Comprising of 16KB monitor EPROMS and 32KB RAM. Battery Backup for RAM. No expansion required

Features:

- The unit is operative on 230V, 50 Hz A.C. Mains
- Strongly Supported by Detailed operating Instructions, giving details of Object, Theory, Design procedures, ReportSuggestions and Book References
- Weight : 5 Kg. (Approx.)
- Power Supply: 5V @ 1A, ± 12V @ 500mA All assembled in an well de signed cabinet

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