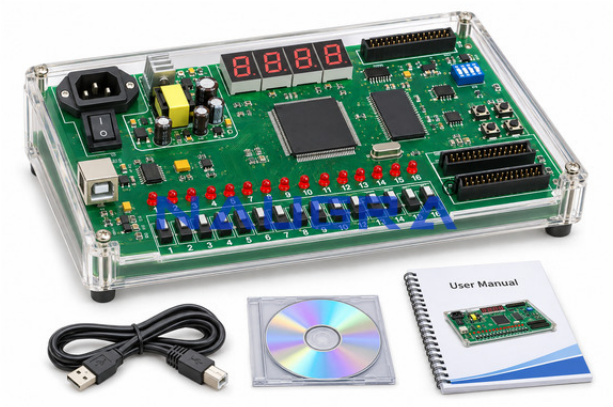
NAUGRA®

Product name:	Product code:
FPGA Development Board Trainer	CMI-NP-1472



Description

FPGA Development Board Trainer is a trainer designed for learning HDL design, logic implementation and VLSI prototyping.

Technical specification

Display	seven segment
Supply	230 V AC, 50 Hz

Standard Accessories

- USB cable
- Software
- Manual

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Features

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- Push buttons, switches and displays
- USB programming
- VGA and expansion ports
- Sample projects
- Compatible HDL tools

Technical Specifications

FPGA	10,000 logic elements
Clock	50 MHz
Memory	8 MB SDRAM
I/O	40 pins
Switches	16, LEDs: 16